



**Cryptographic Hardware and Embedded Systems
- CHES 2004: 6th International Workshop
Cambridge, MA, USA, August 11-13, 2004,
Proceedings (Lecture Notes in Computer Science)**

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This book constitutes the refereed proceedings of the 6th International workshop on Cryptographic Hardware and Embedded Systems, CHES 2004, held in Cambridge, MA, USA in August 2004. The 32 revised full papers presented were carefully reviewed and selected from 125 submissions. The papers are organized in topical sections on side channels, modular multiplication, low resources, implementation aspects, collision attacks, fault attacks, hardware implementation, and authentication and signatures.

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